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- ☐ 2. **A branch-and-bound algorithm for hardware/software partitioning**
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| <input type="checkbox"/> | <p>3. Hardware/software partitioning of embedded systems with multiple hardware processes</p> <p>Hendry, D.C.; Sananikone, D.S.;</p> <p>Computers and Digital Techniques, IEE Proceedings-</p> <p>Volume 144, Issue 5, Sept. 1997 Page(s):285 - 294</p> <p>AbstractPlus Full Text: PDF(984 KB) IEE JNL</p> |
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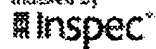
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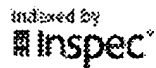
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Edwards, M.; Fozard, B.;
Digital System Design, 2002. Proceedings. Euromicro Symposium on
4-6 Sept. 2002 Page(s):118 - 125
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Harkin, J.; McGinnity, T.M.; Maguire, L.P.;
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4. Hardware/software partitioning for performance enhancement

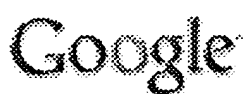
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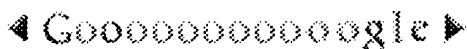
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